

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Magerlein et al.

Serial No.: Not yet assigned

For: HIGH DENSITY AREA ARRAY SOLDER MICROJOINING
INTERCONNECT STRUCTURE AND FABRICATION METHOD

Filed: Concurrently herewith

Examiner: Not yet assigned

Customer No.: 22032

Art Unit: Not yet assigned

Attorney Docket: YOR920010216US2

**MAIL STOP PATENT APPLICATION
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450**

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

In accordance with applicants' duty of disclosure under 37 C.F.R. §1.56, please find attached hereto form PTO-1449 listing information which may be material to the patentability of this application, filed concurrently herewith. This Information Disclosure Statement is being filed:

- XXX Within three (3) months of the filing date of the national application;
- Within three (3) months of the date of entry of the national stage as set forth in 37 C.F.R. §1.491 in an international application;
- Before the mailing date of a first Office Action on the merits;
- After the filing date or date of first Office Action, but before the mailing date of a final action under 37 C.F.R. §1.113, provided that this occurs prior to the issuance of a Notice of Allowance and provided that this I.D.S. is accompanied by either a certification as specified in 37 C.F.R. §1.97(e) or the fee set forth in 37 C.F.R. §1.17(p);

- After the filing date or date of first Office Action, but before the mailing date of a Notice of Allowance under 37 C.F.R. §1.311, provided that this occurs prior to the final action and provided that this I.D.S. is accompanied by either a certification as specified in 37 C.F.R. §1.97(e) or the fee set forth in 37 C.F.R. §1.17(p);
 - After the mailing date of a final action under 37 C.F.R. §1.113, provided that this occurs prior to the issuance of a Notice of Allowance and provided that this I.D.S. is accompanied by a certification as specified in 37 C.F.R. §1.97(e) and the fee set forth in 37 C.F.R. §1.17(p); and
 - After the mailing date of a Notice of Allowance under 37 C.F.R. §1.311, provided that this occurs prior to or subsequent to the payment of the Issue Fee and provided that this I.D.S. is accompanied by a certification as specified in 37 C.F.R. §1.97(e) and the fee set forth in 37 C.F.R. §1.17(p).
- Filing with RCE Under 37 CFR 1.114.

The following references were either (1) cited by or (2) submitted to the Patent and Trademark Office in priority application Serial No. 10/052,620, filed January 18, 2006. Accordingly, copies of the following references have not been submitted herewith. (37 C.F.R. §1.98(d)).

It should be understood that attention has been called to the references that have been deemed to be pertinent to the claimed present invention. In concluding what was pertinent, the criteria employed was considered most appropriate in light of the invention shown in the present application. However, the Examiner or others may deem some other criteria to be just as appropriate or more appropriate. Therefore, the Examiner is respectfully urged to review the listed references and to make the usual careful independent search for other prior art that may be pertinent.

Respectfully submitted,



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October 23, 2003
Date

FORM PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Use several sheets if necessary)		Docket Number (Optional) YOR9200010217US2	Application Number Not yet assigned			
		Applicant Magerlein et al.				
		Filing Date Concurrently herewith	Group Art Unit Not yet assigned			
U. S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6,339,024	1/15/02	Petrarca et al.	438	686	
	6,344,125	2/5/02	Locke et al.	205	118	
	6,368,484	4/9/02	Volant et al.	205	220	
	6,137,185	10-2002	Ishino et al.	257	786	
	6,373,273	04-2002	Akram et al.	324	765	
	5,808,360	09-1998	Akram et al.	257	738	
FOREIGN PATENT DOCUMENTS						
DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
					YES	NO
OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)						
	Richard Volant, Kevin Petrarca, Peter Locke, James A. Tornello and Donald F. Canaperi, Title: "Replated Metal Structures for Semiconductor Devices" , U.S. Patent application Serial No. 09/657,469, filed on May 9, 2000.					
EXAMINER			DATE CONSIDERED			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.						